Compliant with VESA, JEIDA
1/2 configurable port with 1 clock lane and 4
per each port
Configurable sync code detected
data rate up to 1.2Gbps

MIPI DSI/CSI Transmitter

Compliant with D-PHY1.2 & DSI 1.3 & CSI-2
1/2 configurable port
1 clock lane and 1/2/4 configurable data lane
port; 2Gbps per data lane

TTL Transmitter

15-lane SDR/DDR Sampling Support
Max Pixel Clock 37.125MHz

Automotive Display Port Receiver

1/2 configurable link
Bidirectional transmission with maximum 5.4
forward data channel and maximum 3.7Mbps
control channel, only link0, and link1 is only for
channel
Receive video, I2C data and audio from the f
data channel with scrambling, DC balance an
Carry reference clock, I2C data, interrupt and
sync on back control channel with DC balanc
Maximum 10m transmission distance for 5.4
15m for 4.32Gbps

2. General Description

The LT934LMT deserializer is a part of Lontium's long distance video transmission family for Advanced Driver Assistance Systems(ADAS), designed to provide a solution for LVDS, MIPI and TTL video transmission with a coax(POC) or STP cable. The chip delivers a maximum 5.4Gbps forward data channel and back control channel, and supports power over each cable.

3. Applications

Advanced Driver Assistance Systems(ADAS)
Front and Rear Image Sensor
Display System
LVDS/MIPI/TTL Extender

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